



Figure similar

SIMATIC S7-200, CPU 221 Compact unit, AC power supply 6 DI DC/4 DO Relay outputs, 4 KB progr./2 KB data

Supply voltage	
Rated value (AC)	
<ul style="list-style-type: none"> <li>• 120 V AC</li> <li>• 230 V AC</li> </ul>	Yes
Load voltage L+	
<ul style="list-style-type: none"> <li>• Rated value (DC)</li> <li>• permissible range, lower limit (DC)</li> <li>• permissible range, upper limit (DC)</li> </ul>	24 V 5 V 30 V
Load voltage L1	
<ul style="list-style-type: none"> <li>• Rated value (AC)</li> <li>• permissible range, lower limit (AC)</li> <li>• permissible range, upper limit (AC)</li> <li>• permissible frequency range, lower limit</li> <li>• permissible frequency range, upper limit</li> </ul>	100 V; 100 V AC to 230 V AC 5 V 250 V 47 Hz 63 Hz
Input current	
Inrush current, max.	20 A; at 264 V
from supply voltage L1, max.	120 mA; 15 to 60 mA (240 V); 30 to 120 mA (120 V); output current for expansion modules (5 V DC) 340 mA
Encoder supply	
24 V encoder supply	
<ul style="list-style-type: none"> <li>• 24 V</li> <li>• Short-circuit protection</li> <li>• Output current, max.</li> </ul>	Yes; Permissible range: 20.4V to 28.8V Yes; electronic at 600 mA 180 mA
Power loss	
Power loss, typ.	6 W
Memory	
Number of memory modules (optional)	1; pluggable memory module, content identical with integral EEPROM; can additionally store recipes, data logs and other files
Work memory	
<ul style="list-style-type: none"> <li>• integrated (for program)</li> <li>• integrated (for data)</li> </ul>	4 kbyte 2 kbyte
Backup	
<ul style="list-style-type: none"> <li>• present</li> </ul>	Yes; Program: Entire program maintenance-free on integral EEPROM, programmable via CPU; data: Entire DB 1 loaded from PG/PC maintenance-free on integral EEPROM, current values of DB 1 in RAM, retentive memory bits, timers, counters, etc. maintenance-free via high-performance capacitor; optional battery for long-term buffering
Battery	
Backup battery	

• Backup time, max.	50 h; (min. 8 h at 40 °C); 200 days (typ.) with optional battery module
<b>CPU processing times</b>	
for bit operations, max.	0.22 µs
<b>Counters, timers and their retentivity</b>	
<b>S7 counter</b>	
• Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— lower limit	1
— upper limit	256
Counting range	
— lower limit	0
— upper limit	32 767
<b>S7 times</b>	
• Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— upper limit	64
Time range	
— lower limit	1 ms
— upper limit	54 min; 4 timers: 1 ms to 30 s; 16 timers: 10 ms to 5 min; 236 timers: 100 ms to 54 min
<b>Data areas and their retentivity</b>	
Flag	
• Size, max.	32 byte
• Retentivity available	Yes; M 0.0 to M 31.7
• of which retentive with battery	0 to 255, via high-performance capacitor or battery, adjustable
• of which retentive without battery	0 to 112 in EEPROM, adjustable
<b>Hardware configuration</b>	
connectable programming devices/PCs	SIMATIC PG/PC, standard PC
<b>Digital inputs</b>	
Number of digital inputs	6; Integrated
Source/sink input	Yes; optionally, per group
Input voltage	
• Rated value (DC)	24 V
• for signal "0"	0 to 5 V
• for signal "1"	min. 15 V
Input current	
• for signal "1", typ.	2.5 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; all
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes; I 0.0 to I 0.3
for technological functions	
— parameterizable	Yes; (E 0.0 to E 0.5) 30 kHz
Cable length	
• shielded, max.	500 m; Standard input: 500 m, high-speed counters: 50 m
• unshielded, max.	300 m; not for high-speed signals
<b>Digital outputs</b>	
Number of digital outputs	4; Relays
Short-circuit protection	No; to be provided externally
Switching capacity of the outputs	
• with resistive load, max.	2 A
• on lamp load, max.	30 W with DC, 200 W with AC
Output voltage	
• for signal "1", min.	L+/L1

<b>Output current</b>	
<ul style="list-style-type: none"> <li>for signal "1" rated value</li> <li>for signal "0" residual current, max.</li> </ul>	2 A 0 mA
<b>Output delay with resistive load</b>	
<ul style="list-style-type: none"> <li>"0" to "1", max.</li> <li>"1" to "0", max.</li> </ul>	10 ms; all outputs 10 ms; all outputs
<b>Parallel switching of two outputs</b>	
<ul style="list-style-type: none"> <li>for uprating</li> </ul>	No
<b>Total current of the outputs (per group)</b>	
all mounting positions	
— up to 40 °C, max.	6 A
horizontal installation	
— up to 55 °C, max.	6 A
<b>Relay outputs</b>	
<ul style="list-style-type: none"> <li>Number of relay outputs</li> <li>Number of operating cycles, max.</li> </ul>	4 10 000 000; mechanically 10 million, at rated load voltage 100 000
<b>Cable length</b>	
<ul style="list-style-type: none"> <li>shielded, max.</li> <li>unshielded, max.</li> </ul>	500 m 150 m
<b>Analog inputs</b>	
Number of analog potentiometers	1; Analog potentiometer; resolution 8 bit
<b>Encoder</b>	
<b>Connectable encoders</b>	
<ul style="list-style-type: none"> <li>2-wire sensor</li> <li>— permissible quiescent current (2-wire sensor), max.</li> </ul>	Yes 1 mA
<b>1. Interface</b>	
Interface type	Integrated RS 485 interface
<b>Protocols</b>	
<ul style="list-style-type: none"> <li>MPI</li> <li>PPI</li> <li>serial data exchange</li> </ul>	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s  Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication ; transmission rates 9.6/19.2/187.5 kbit/s  Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
<b>MPI</b>	
<ul style="list-style-type: none"> <li>Transmission rate, min.</li> <li>Transmission rate, max.</li> </ul>	19.2 kbit/s 187.5 kbit/s
<b>Integrated Functions</b>	
<b>Counter</b>	
<ul style="list-style-type: none"> <li>Number of counters</li> <li>Counting frequency, max.</li> </ul>	4; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.  30 kHz
Number of alarm inputs	4; 4 rising edges and/or 4 falling edges
<b>Potential separation</b>	
<b>Potential separation digital inputs</b>	
<ul style="list-style-type: none"> <li>between the channels</li> <li>between the channels, in groups of</li> </ul>	Yes 2 and 4
<b>Potential separation digital outputs</b>	
<ul style="list-style-type: none"> <li>between the channels</li> <li>between the channels, in groups of</li> </ul>	Yes; Relays 1 and 3
<b>Permissible potential difference</b>	
between different circuits	500 V DC between 24 V DC and 5 V DC; 1500 V AC between 24 V DC and 230 V AC

Degree and class of protection	
IP degree of protection	IP20
Ambient conditions	
Ambient temperature during operation	
• horizontal installation, min.	0 °C
• horizontal installation, max.	55 °C
• vertical installation, min.	0 °C
• vertical installation, max.	45 °C
Air pressure acc. to IEC 60068-2-13	
• permissible range, lower limit	860 hPa
• permissible range, upper limit	1 080 hPa
Relative humidity	
• Operation, min.	5 %
• Operation, max.	95 %; RH class 2 in accordance with IEC 1131-2
configuration / header	
configuration / programming / header	
• Command set	Bit logic instructions, compare instructions, timer instructions, counter instructions, clock instructions, transmissions instructions, table instructions, logic instructions, shift and rotate instructions, conversion instructions, program control instructions, interrupt and communications instructions, logic stack instructions, integer maths, floating-point math instructions, numerical functions
• Program processing	free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms)
• Program organization	1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer
• Number of subroutines, max.	64
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
Know-how protection	
• User program protection/password protection	Yes; 3-stage password protection
connection method / header	
Plug-in I/O terminals	No
Dimensions	
Width	90 mm
Height	80 mm
Depth	62 mm
Weights	
Weight, approx.	310 g
<b>last modified:</b>	3/12/2021 